



VLSI Training

CORPITS
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A Division of CSE Group

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VLSI

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- 1. Advance Digital Design**
 - Boolean Algebra and Logic Gates
 - Boolean Functions
 - Combinational Logic
 - Sequential Logic
 - State Machine Design
- 2. Basics of CMOS VLSI Design**
 - Introduction to Digital Circuits
 - Basic CMOS Technology
 - Circuit Characterisation
 - Performance Optimisation
 - Power Dissipation
- 3. VHDL / Verilog**
 - Behavioural Modeling
 - Dataflow Modeling
 - Structural Modeling
 - Simulation and Synthesis
- 4. Hardware Implementation and Verification**
- 5. Projects in VLSI**
- 6. EDA Tools**
 - ModelSim 6.2a
 - Quartus II
 - Xilinx ISE 9.2i Foundation Series

Duration

60 Hours

Very-Large-Scale Integration (VLSI)